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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10/563,285-Conf. #4334	
				Filing Date	January 3, 2006	
				First Named Inventor	Yoshitoshi Kida	
				Art Unit	2629	
	(Use as many sheets as necessary)			Examiner Name	Richard A. Hjerpe	
Sheet	1	of	1	Attorney Docket Number	SON-3056	

U.S. PATENT DOCUMENTS					
Examiner	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where
Initials*	No.1	Number-Kind Code ² (if known)	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear
	AA	US-US 5,572,153	11-05-1996	Vallancourt et al.	

FOREIGN PATENT DOCUMENTS						
Examiner	Cite No.1	Foreign Patent Document	Publication	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	
Initials*		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Date MM-DD-YYYY			T ⁶
	BA	JP-2003-150109 A	05-23-2003	Matsushita Electric Ind Co Ltd		
	BB	JP-09-330060 A	12-22-1997	Toshiba Corp		
	ВС	JP-2002-152565 A	05-24-2002	Canon Inc.		
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Examiner	Date	•
Signature	Considered	

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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journ al, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	CA	Japanese Office Action for application no. 2003-272443 dated July 1, 2008.	1
		Daubert et al., "Operation and analysis of current copier circuits", IEEE Proceedings, Vol., 137, Pt. G, No. 2, April 1990.	

Examiner	Date	
Signature	Considered	

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